

FIG. 1

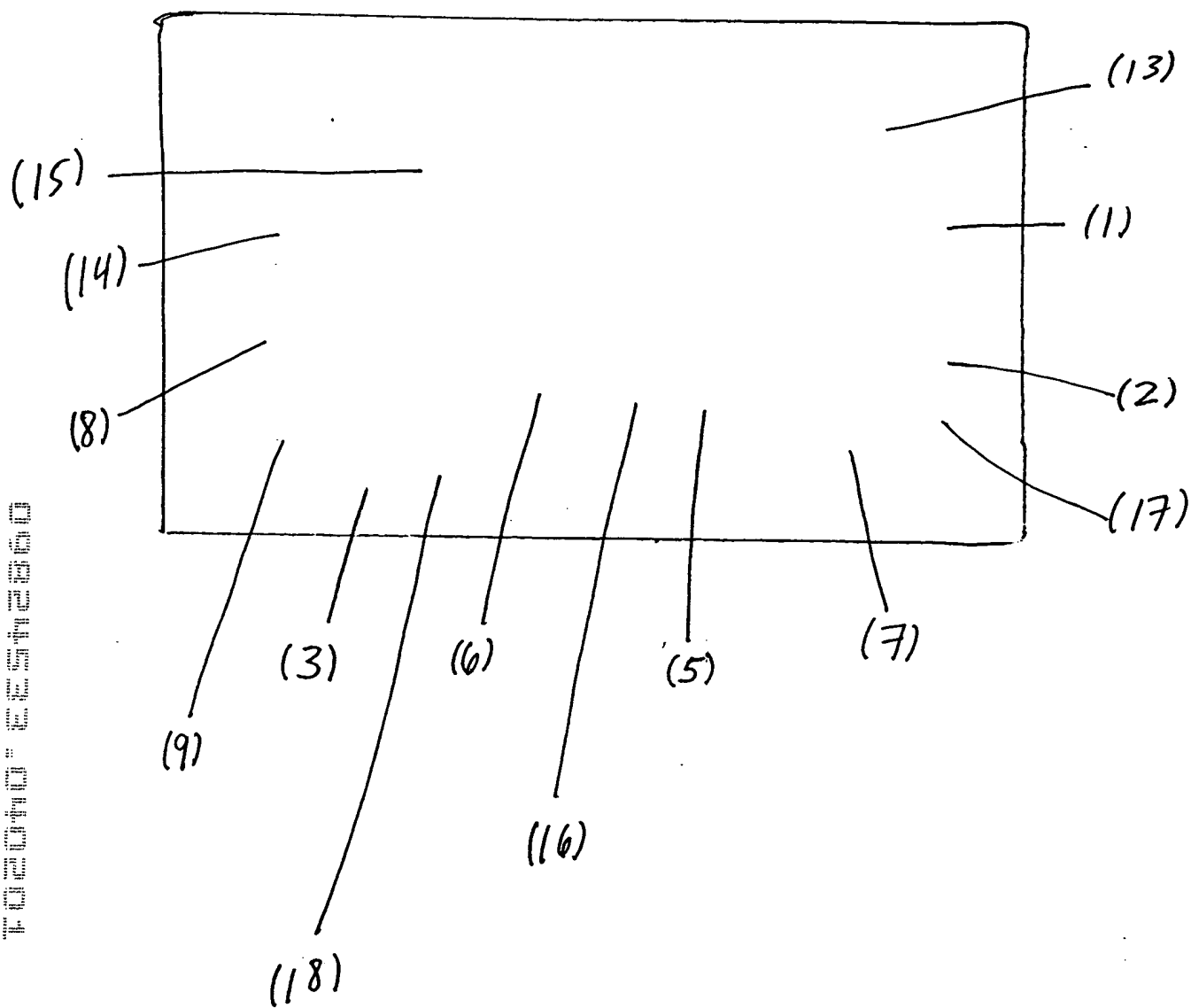


Figure 1

FIG. 2 is a schematic diagram of a system for

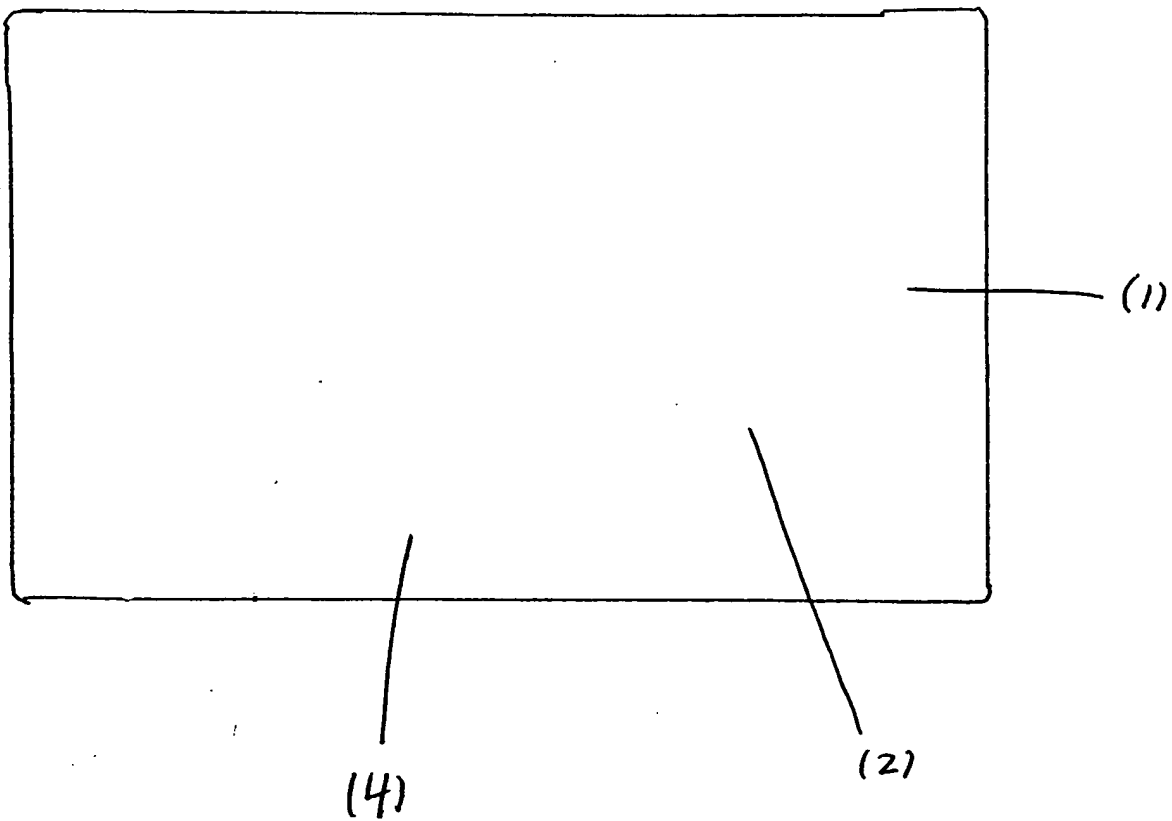


Figure 2

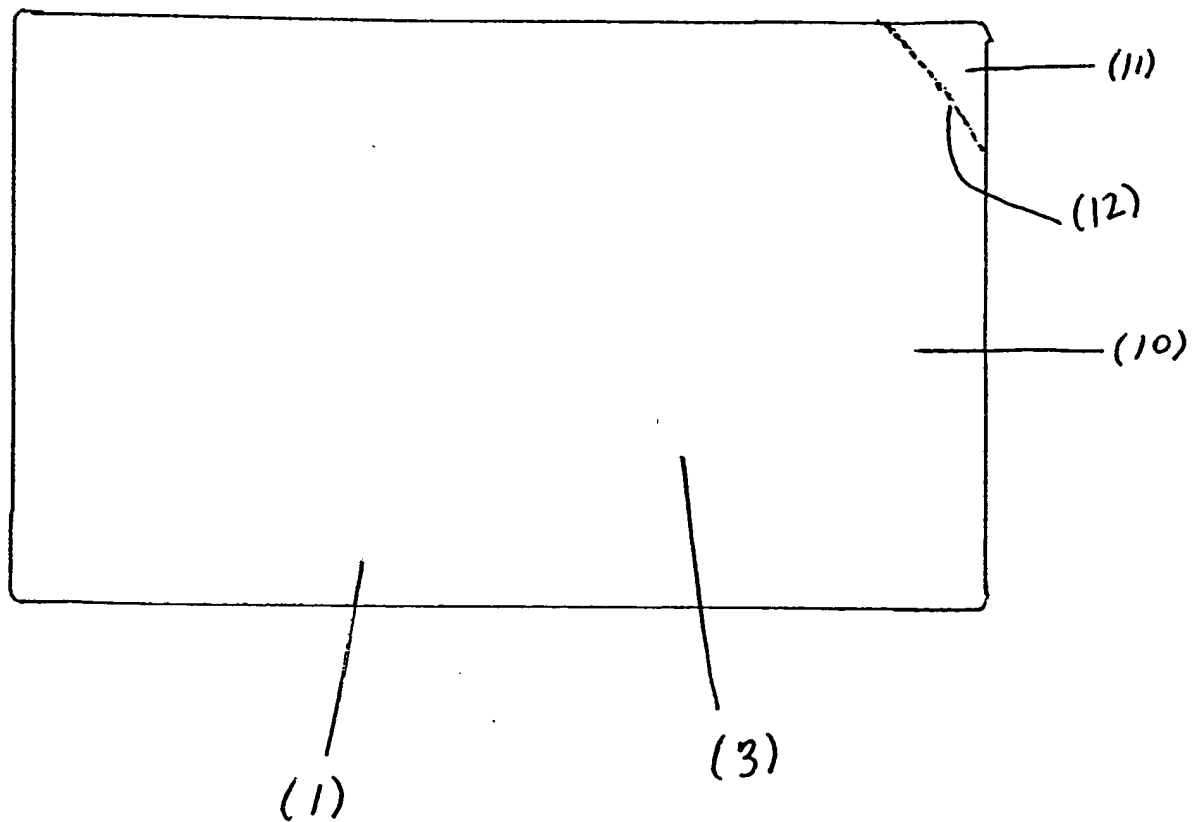


Figure 3

FIG. 4

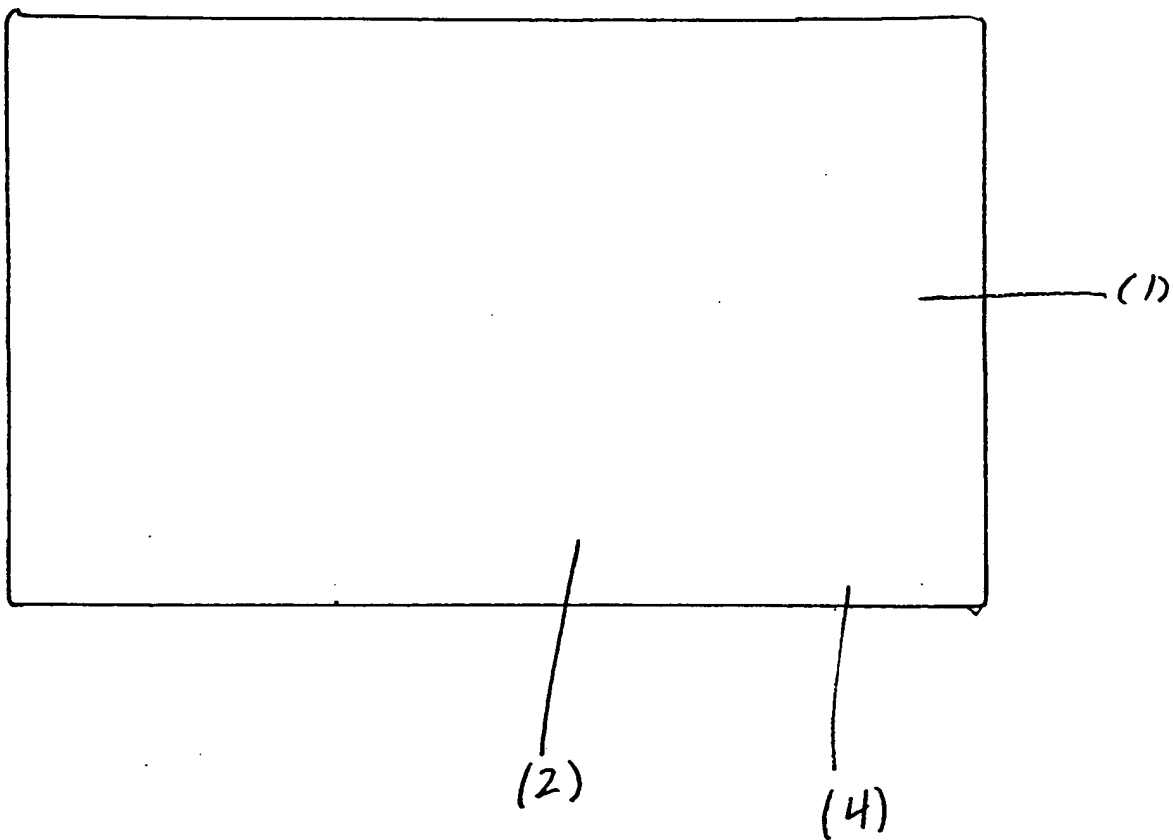


Figure 4

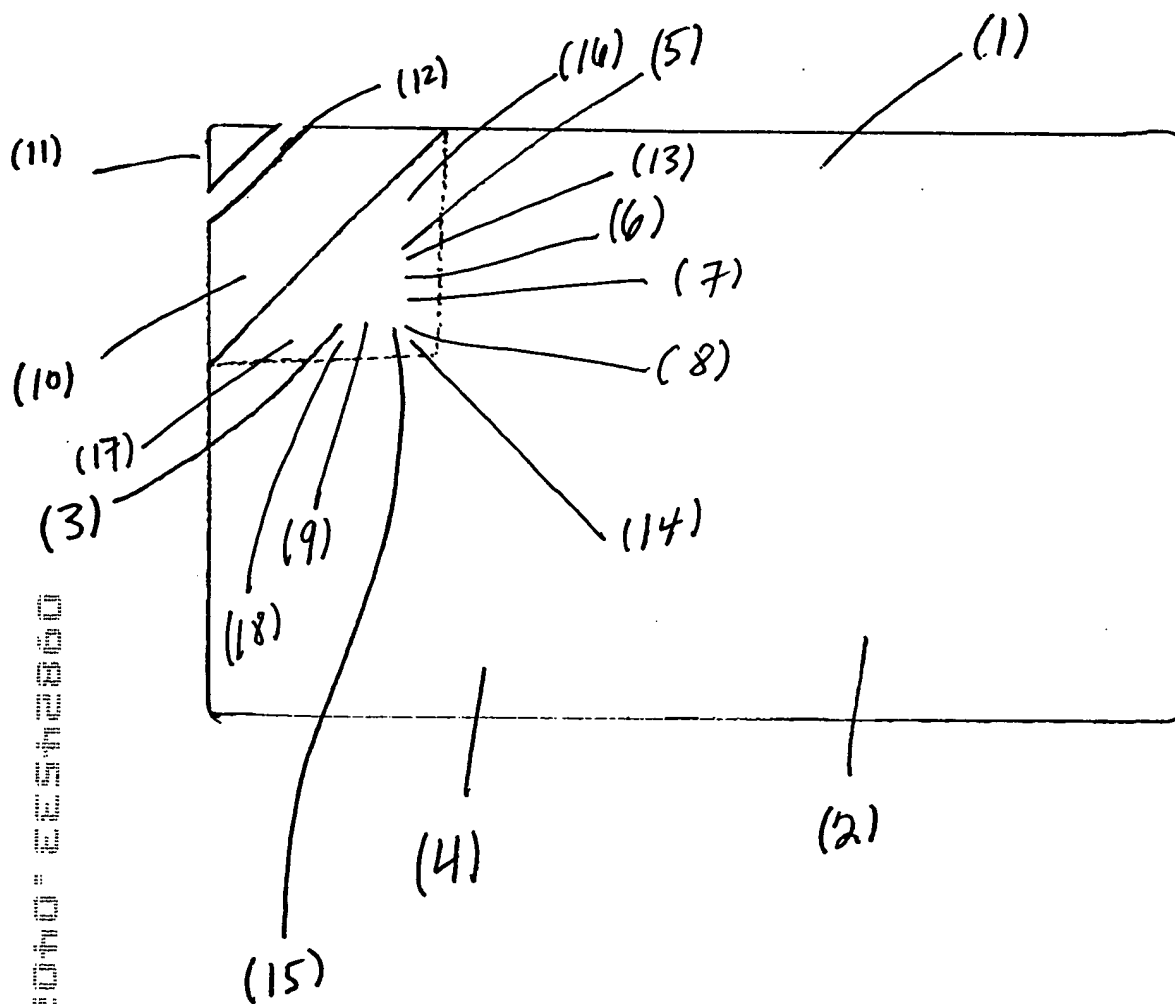


Figure 5

FIG. 6

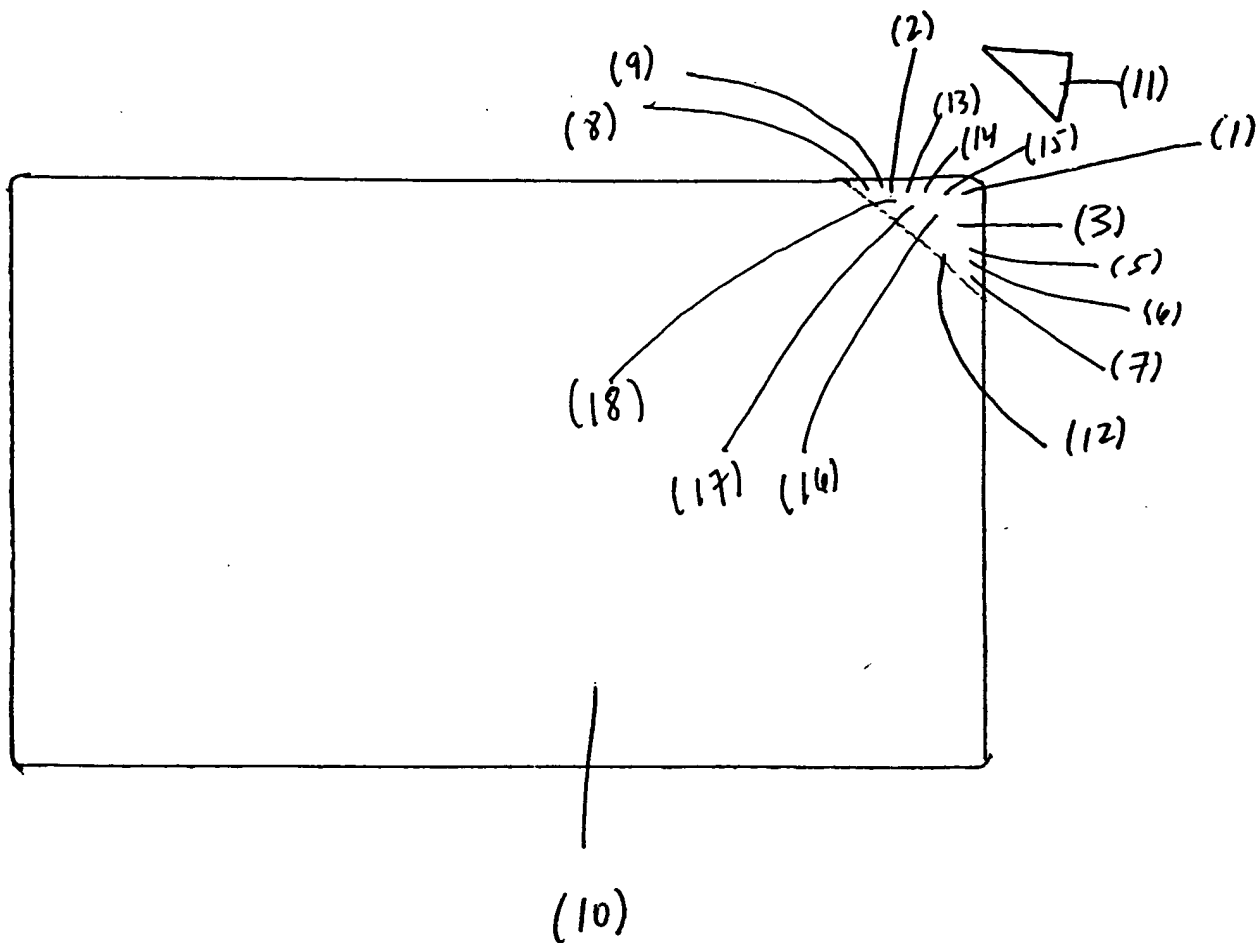


Figure 6

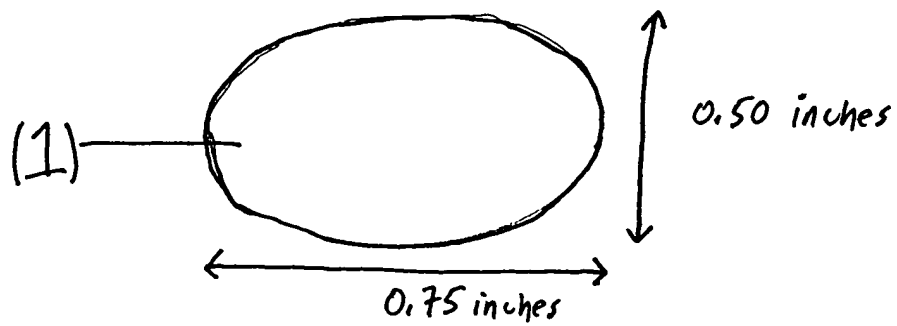


Figure 7

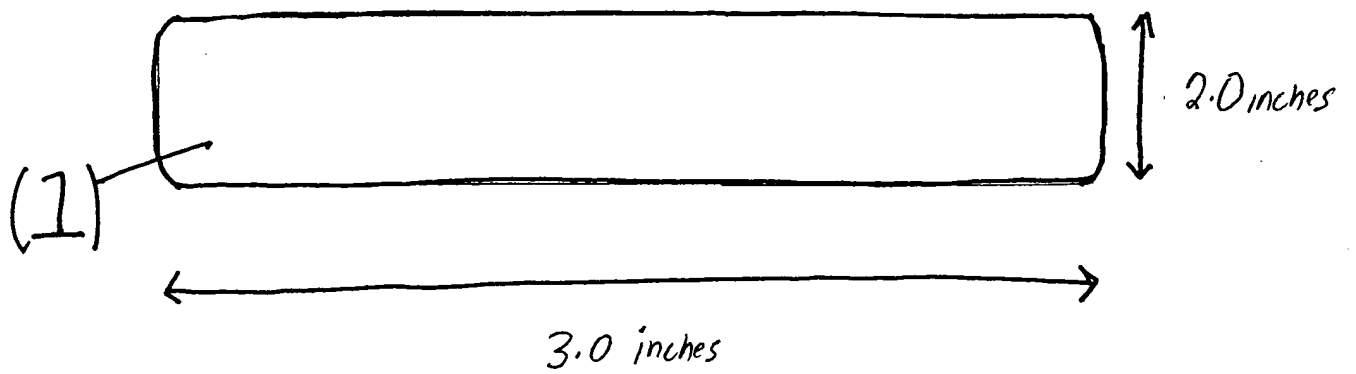


Figure 8

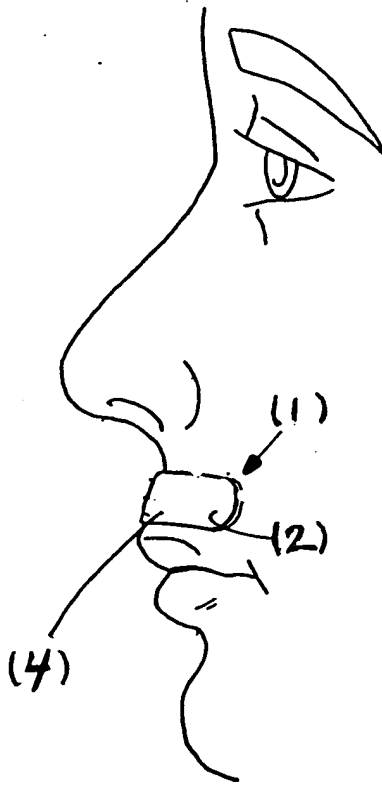


Figure 9

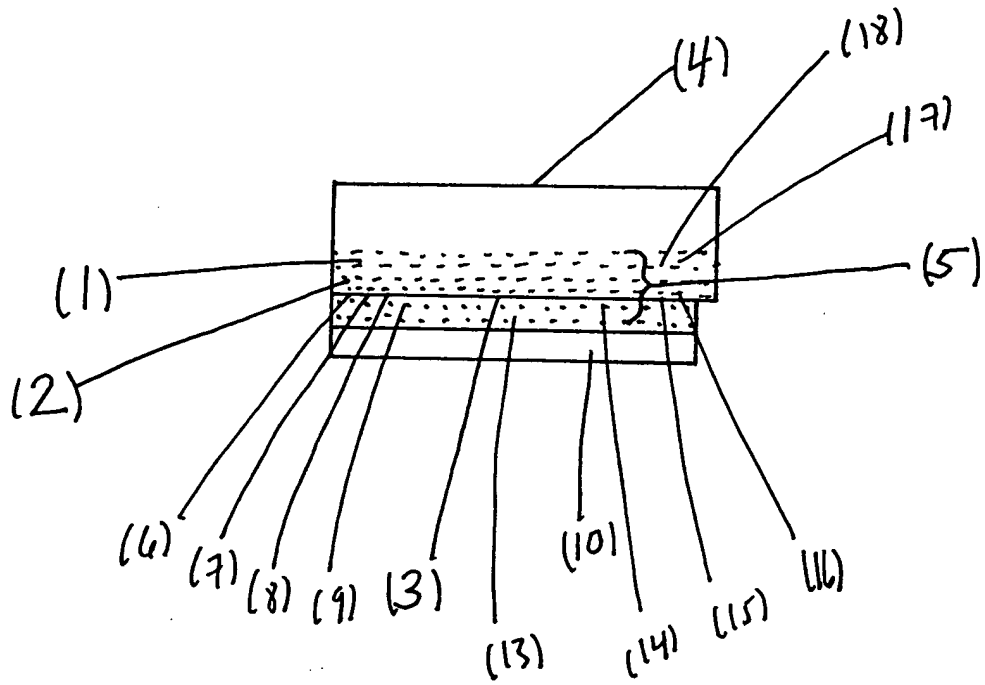


Figure 10

Figure 11

1

